Systems and Software Verification

### Chapter 4. Symbolic Model Checking

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# 4. Symbolic Model Checking

- $\bullet$  $\bullet$  Symbolic model checking
	- Any model checking method attempting to represent symbolically states and transitions
	- A particular symbolic method in which BDDs are used to represent the state variables
		- BDD : Binary Decision Diagram
- • Motivation:
	- –– State explosion is the main problem for CTL or PLTL model checking
	- State explosion occurs whenever we represent explicitly all states of automaton we use
	- Represent very large sets of states concisely, as if they were in bulk.
- • Organization of chapter 4
	- Symbolic Computation of State Sets
	- Binary Decision Diagrams (BDD)
	- Representing Automata by BDDs
	- BDD-based Model Checking

# 4.1 Symbolic Computation of State Sets

- •• Iterative computation of  $Sat(\phi)$ 
	- *A* = < *Q*, *T*, … <sup>&</sup>gt;
	- *Pre(S)* : immediate predecessors of the states belonging to *S* in *Q*
	- *Sat(ф)* : set of states of *A* which satisfy *ф*
	- *ψ* is the sub-formulas of *ф*
	- $Sat(\neg \psi) = Q \setminus Sat(\psi)$
	- *Sat(ψ* ∧ *ψ') = Sat(ψ)*  ∩ *Sat(ψ')*
	- *Sat(EX ψ) = Pre(Sat(ψ))*
	- *Sat(AX ψ) = Q \ Pre(Q \ Sat(ψ))*
	- *Sat(EF ψ) = Pre\*(Sat (ψ))*
	- … (others are defined in a similar way)

```
\gamma^* == == Computation of Pre*(S) ==== */
X := S;
Y := \{\}while (Y := X) {
  Y := X;X := X \vee Pre(X);}
return X;
```
- The algorithms in Section 3.1 is an particular implementation of *Sat(ф)*
- $-$  Hence,  $Sat(\phi)$  is an explicit representation of the state sets
- $\bullet$  Which symbolic representations to use ?
	- We have to access the following primitives:
		- 1. A symbolic representation of  $Sat(P)$  for each proposition  $P \in Prop$ ,
		- 2. An algorithm to compute a symbolic representation of *Pre(S)* from a symbolic representation of *S*,
		- 3. Algorithms to compute the complement, the union, and the intersection of the symbolic representations of the sets,
		- 4. An algorithm to tell whether two symbolic representations represent the same set.
- Which logic for symbolic model checking?
	- Logics based on state formulas
	- CTL is the best.
	- Mu-calculus on tree is possible.
- $\bullet$  Systems with infinitely many states
	- $-$  Symbolic approach naturally extends to infinite systems.
	- New difficulties:
		- 1. Much trickier to come up with symbolic representations
		- 2. Iterative computation *Sat(ф)* is no longer guaranteed to terminate.

# 4.2 Binary Decision Diagram (BDD)

#### $\bullet$ BDD

- A particular data structure very commonly used for representing states sets symbolically
- Proposed in 1980s ~ early in 1990s
- –Make possible the verification of the system which cannot represent explicitly.
- – Advanta ges:
	- 1. Efficiency
	- 2. Simplicity
	- 3. Easy Adaptation
	- 4. Generality
- $\bullet$ • BDD structure
	- Example
		- $\bullet$ • Consider n boolean variables  $x_1, x_2, ..., x_n$  associated with a tuple  $\lt b_1, b_2, ..., b_n$
		- $\bullet$ Suppose  $n = 4$ ,
		- The set S of our interest is the set such that  $(b_{1}\vee b_{3})\wedge (b_{2}\Rightarrow b_{4})$  is true.
		- We have several ways to represent the set:
			- S = {<F,F,T,F>, <F,F,T,T> , … >
			- $S = (b_1 \vee b_2) \wedge (b_3 \Rightarrow b_4)$

• 
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S = (b_1 \wedge \neg b_2) \vee (b_1 \wedge b_4) \vee (b_3 \wedge \neg b_2) \vee (b_3 \wedge b_4)
$$
  $\leftarrow$  DNF

- •…
- •• Decision Tree  $\leftarrow$  Our choice.



- $\bullet$  Decision tree reduction
	- –A BDD is a reduced decision tree.
	- – Reduction rules:
		- 1. Identical sub-trees are identified and shared. ( $n_g$  and  $n_{\scriptscriptstyle{10}}$ )  $\rightarrow$  leads to a directed acyclic graph (dag)
		- 2. Superfluous internal nodes are deleted.  $(n<sub>7</sub>)$
	- – Advantages:
		- 1. Space saving
		- 2. Canonicity



- $\bullet$  Canonicity of BDDs
	- –BDDs canonically represent sets of boolean tuples. (fundamental property of BDDs)
	- – $-$  If the order of the variable  $x_i$  is fixed, then there exists a unique BDD for each set  $S$ .
	- – Properties of BDDs
		- 1. We can test the equivalence of two BDDs in constant time.
		- 2. We can tell whether a BDD represents the empty set simply by verifying whether it is reduced to a unique leaf F.
- $\bullet$ **Operations on BDDs** 
	- All boolean operations
		- 1. Emptiness test
		- 2. Comparison
		- 3.Complementation
		- 4. Intersection
		- 5. Union and other binary boolean operations
		- 6. Projection and abstractions
	- –Complexity : linear or quadratic (for each operation)
		- $\rightarrow$  the same state explosion problems still exist.

### 4.3 Representing Automata by BDDs

- $\bullet$ • Before applying BDDs to symbolic model checking, we need to restate
	- Representing the states by BDDs
	- Representing transitions by BDDs
- • Representing the states by BDDs
	- Consider an automaton *A* with
		- $Q = \{q_0, \dots, q_6\} \rightarrow b^1, b^2, b^3$
		- var digit:0..9  $\rightarrow b^1_2, b^2_2, b^3_2, b^4_2$
		- var ready:bool  $\rightarrow b^1_{3}$
		- **,**  $b^2$ **,**  $b^3$ **,**  $b^1$ **,**  $b^2$ **,**  $b^2$ **,**  $b^3$ **,**  $b^4$ **,**  $b^1$ **,**  $b^1$
		- $\langle F, T, T, T, F, F, F, F \rangle = \langle q_3, 8, F \rangle$
	- – Let's represent *Sat*(read y ⇒ (digit > 2))
		- States  $\lt q, k, b$  such that if  $b = T$  and  $k > 2$
		- ready ⇒ (digit > 2) ≡ ¬ ready ∨ (digit > 2)



- • Representing transitions by BDDs
	- The same idea is applied.
	- $\langle q_{3}, 8, F \rangle \rightarrow \langle q_{5}, 0, F \rangle$  :  $\langle F, T, T, T, F, F, F, F, F, T, F, F, F, F, F \rangle$
	- For example,



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(,   
\n $\rightarrow q = q_1, k \neq 0, q' = q_2, k' = k, b' = T$
$$

$$
\begin{array}{l} \n\rightarrow (\ \neg b_1^1 \wedge \neg b_1^2 \wedge b_1^3) \\ \wedge \ (\ b_2^1 \vee b_2^2 \vee b_2^3 \vee b_2^4) \\ \wedge \ (\ \neg b'{}_1^1 \wedge \neg b'{}_1^2 \wedge b'{}_3^1) \\ \wedge \ (\ b'{}_2^1 \Leftrightarrow b_2^1 \wedge b'{}_2^2 \Leftrightarrow b_2^2 \wedge b'{}_3^2 \Leftrightarrow b_2^3 \wedge b'{}_2^4 \ b_2^4) \\ \wedge \ b'{}_3^1 \end{array}
$$

# 4.4 BDD-based Model Checking

- •BDDs can serve as an instance of symbolic model checking scheme
	- Provide compact representations for the sets of states in an automata
	- Support the basic sets of operations
	- Computation of *Pre(S)* in section 4.1 is very simple

### • Implementation

- SMV (chapter 12)
- Efficiency of BDDs depends on
	- $B_T$  representing the transition relation  $T$  (as containing pairs of states)
	- Choice of ordering for the boolean variables
- Very easy to explode exponentially

#### •Perspective

- Widely used from early 1990s
- Current work on model checking
	- Aiming at applying BDD technology to solve more verification problems (ex. program equivalence)
	- Aiming at extending the limits inherent to BDD-based model checking
- Widely used throughout the VLSI design industry