Introduction to Formal Specification

JUNBEOM YOO jbyoo@knokuk.ac.kr http://dslab.konkuk.ac.kr

Reference

- " A Specifier's Introduction to Formal Methods "
 - Jeannette M. Wing, Carnegie Mellon University
 - IEEE COMPUTER, 1990



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Contents

- Overview of Formal Methods
- Formal Specification Language
- Pragmatics
- Some Examples
- Bounds of Formal Methods
- Concluding Remarks

Overview of Formal Methods

- Definition
- Features
- Applying Scope
- Pragmatic Considerations

Definition

• Formal Methods

- Mathematically based techniques for describing system properties
 - Have a sound mathematical basis
 - Typically given by a formal specification language
- Provide frameworks for systematically
 - <u>Specifying</u>,
 - <u>Developing</u>, and
 - <u>Verifying</u> systems

Features

- Formal methods provide <u>means of precisely defining</u> notions like
 - Completeness
 - Consistency
 - Specification
 - Implementation
 - Correctness
- Formal methods address a number of pragmatic considerations
 - Who
 - What
 - When
 - How it is used?
 - ex) <u>System designers</u> use <u>a formal method</u> to specify a system's desired behavioral and structural properties.

Applying Scope

- Any stage of system development can make use of formal methods
 - 1. Initial statement of a customer's requirements
 - 2. System design
 - 3. Implementation
 - 4. Testing
 - 5. Debugging
 - 6. Maintenance
 - 7. Verification
 - 8. Evaluation
- When used early,
 - Can reveal design flaws
- When used later,
 - Can help determine the correctness of a system implementation
 - Can help determine the equivalence of different implementations

Pragmatic Considerations

- Pragmatic considerations
 - A set of guidelines
 - Formal methods should tell the user
 - 1. Circumstances under which the method should and can be applied
 - 2. How it can be applied most effectively
 - Formal Specification
 - One tangible product of applying formal methods
 - More precise and concise than informal specifications
 - A formal method's specification language may have <u>Tool Supports</u>
 - 1. Syntax analysis
 - 2. Semantic analysis with machine aids

Formal Specification : Use mathematics to specify the desired properties of a computer system with support of automatic tools

Formal Specification Language

- Definition
- Syntactic Domains
- Semantics Domains
- Satisfies Relation
- Properties of Specifications
- Proving Properties of Specificands

Definition

• Formal specification language:

- < Syn, Sem, Sat >, where
 - *Syn* : syntactic domain
 - *Sem* : semantic domain
 - Sat: Sat \subseteq Syn X Sem
 - syn is a specification of sem
 - sem is a specificand of syn
- Considerations
 - In principle, a formal method is based on some well-defined formal specification language
 - Formal specification language provides a formal method's mathematical basis
 - Formal methods differ because their specification languages have different syntactic and/or semantic domains

Syntactic Domains

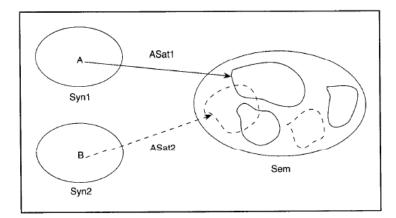
- Syn
 - a set of symbols
 - Constants
 - Variables
 - Logical connectives
 - <u>a set of grammatical rules</u> for combining symbols into well-formed sentences (semantics)
 - Ex) $\forall x.P(x) \Rightarrow Q(x)$: correct!! $\forall x. \Rightarrow P(x) \Rightarrow Q(x)$: wrong!!
 - Visual Specification : Graphical elements are also available
 - boxes, circles
 - lines, arrows
 - called **Specification**

Semantic Domains

- Sem
 - Formal specification languages differ most in their choice of semantic domains (Specificand) such as:
 - Abstract-data-type specification languages
 - algebra, theory, program
 - Concurrent and distributed systems specification languages
 - state sequence, event sequence, state and transition sequence
 - stream, synchronization tree, partial order
 - state machine
 - Programming languages
 - function from input to output, computation
 - predicate transformation
 - relation, machine instruction
 - called Implementation

Satisfies Relation

- Sat
 - Specifies different aspects of a single specificand using different specification languages:
 - 1. <u>Behavioral specification aspect</u>
 - Constraints on observable behavior of specificands
 - System's required functionality (mapping from inputs to outputs)
 - Others: fault tolerance, safety, security, response time, space efficiency
 - 2. <u>Structural specification aspect</u>
 - Constraints on the internal composition of specificands
 - Various hierarchical and uses relations
 - Call graph, data-dependency diagram, definition-use chain



Properties of Specifications

- Specification language should be defined as
 - 1. Unambiguous
 - If and only if it has exactly one meaning
 - Any natural languages and graphs are not formal inherently
 - 2. Consistent
 - If and only if its specificand set is non-empty
 - Cannot derive anything contradictory from the specification
 - There is some implementation that will satisfy the specification
 - 3. Complete
 - Need not be complete in the sense used in mathematical logic
 - Relatively-completeness properties might be desirable
 - In practice, we must usually deal with incomplete specifications
- A specification has <u>implementation bias</u> if it places unnecessary constraints on its specificand

Proving Properties of Specifications

- Most formal specification languages have <u>logical inference systems</u>
 - Can prove properties from the specification about specificands
 - Can predict system's behavior without executing or building the system
 - Can be mechanized
 - Theorem proving
 - Model checking
 - called Formal Verification (Part II)

Pragmatics

Users Uses Characteristics

Users

- 5 kind of users
 - 1. Specifier : write, evaluate, analyze, and refine specifications
 - 2. Customer : hired the specifiers
 - 3. Implementer : realize a specification
 - 4. Client : use a specified system
 - 5. Verifier : prove the correctness of implementations
- A formal method's <u>guidelines</u> should identify
 - 1. Different types of users the method is targeted for
 - 2. Capabilities the users should have
 - 3. Application domain of the method

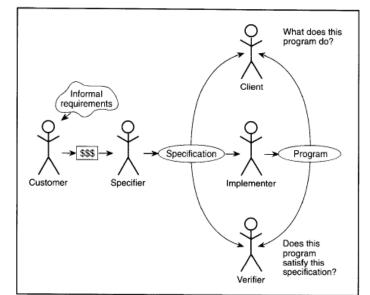


Figure 2. Specification users.



Uses

- The greatest benefit comes
 - from the process of formalizing
 - rather than the end result
- Can apply formal methods in <u>all phases</u> of SW development
 - 1. Requirements analysis
 - 2. System design
 - 3. System verification
 - 4. System validation
 - 5. System documentation
 - 6. System analysis and evaluation
- These applications should be considered as an integral one, framework

Uses 1. Requirements Analysis

- Formal methods help clarify customer's informally stated requirements
 - Crystallize customer's vague ideas
 - Reveal
 - Contradictions,
 - Ambiguities, and
 - Incompleteness in the requirements
- On the specification, both customers and specifiers can see
 - Whether it reflects customer's intuition
 - Whether specificand set has desired set of properties

Uses 2. System Design

- Two important activities during design
 - 1. Decomposition
 - 2. Refinement
- Decomposition
 - Process of partitioning a system into smaller modules
 - Interface specifications specify interfaces between modules
- <u>Refinement</u>
 - Process of refining modules at one level to modules at a lower level
 - Each refinement step should prove that a specification(program) at one level satisfies a higher level specifications
 - Program transformation, Program synthesis, Inferential programming
 - Formal methods and formal specification languages can state proof obligations(assumptions) precisely

Uses 3. System Verification

- System verification
 - Showing that a system satisfies its specification
- Formal Verification
 - Using formal specifications to verify a system
 - Cannot completely verify an entire system,
 - But can certainly verify smaller and critical part of system.
 - Gypsy, HDM(Hierarchical Development Method), FDM(Formal Development Method)
 - M-EVES(Environment for Verifying and Emulating Software)
 - HOL(Higher Order Logic)
- Difficulties in formal system verification
 - Should state explicitly assumptions about its environment : Not easy!
 - "Bounds of Formal Methods"

Uses 4. System Validation

- Formal methods can aid in system testing and debugging
- Specification alone :
 - Used to generate test cases for black-box testing
 - For boundary condition tests
- Specification along with implementation
 - Used to generate test cases
 - Additionally, can be used for testing analysis
 - Path testing
 - Unit testing
 - Integration testing
 - Etc.

Uses 5. System Documentation

- Formal specification
 - Captures "What" rather than "How"
 - Serves as a communication medium between
 - Clients and Specifiers
 - Specifiers and Implementers
 - Among members of an implementation team

Uses 6. System Analysis and Evaluation

- System analysis and evaluation
 - After system has been built and tested,
 - Critical analysis of its functionality and performance should be done
 - Does the system do what the customer wants?
 - Does it do it fast enough?
 - Formal method used in the development can help formulate and answer these questions
- Most formal methods have not yet been applied to specifying largescale software and hardware systems
 - Size of the specification
 - Complexity of the specificand
 - Internal complexity
 - Interface complexity

Characteristics

- Formal method's characteristics influence the style in which a user applies it
 - Whether its language is graphical or textual
 - Whether its underlying logic is first-order or high-order
 - Etc.
- Formal method reflects a combination of many different characteristics:
 - 1. Model-oriented vs. Property-oriented
 - 2. Visual languages
 - 3. Executable
 - 4. Tool-supported

Characteristics 1. Model-oriented vs. Property-oriented

- <u>Model-oriented methods</u>
 - Define system's behavior <u>directly</u> by constructing a model of the system
 - 1. For sequential systems
 - Parnas' statemechines, VDM, Z, SCR, NuSCR
 - 2. For concurrent and distributed systems
 - Petri Nets, CCS, Hoare's CSP, Unity, I/O automata
 - Temporal logic, Lamport's transition axiom method, LOTOS
- Property-oriented methods
 - Define system's behavior indirectly by stating a set of properties using axioms
 - 1. Axiomatic methods
 - Iota, OBJ, Anna, Larch
 - 2. Algebraic methods
 - Act One

Algebraic specification of abstract data types can handle :

- Error values
- Nondeterminism
- parameterization

Characteristics 2. Visual Languages

- Visual specification languages
 - Any one who contains graphical elements in their syntactic domains
- Many examples
 - Petri nets : for concurrent systems
 - Statecharts : for specifying state transitions in reactive systems
- Semiformal methods
 - Multiple interpretations or text attached
 - Jackson's method (UML)
 - SASD, OOD
 - Requirements Engineering Methodology

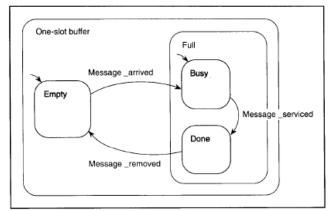


Figure 3. State chart specification of a one-slot buffer.

Characteristics 3. Executable

- Executable Specification
 - Can run on a computer
- Specifiers can use executable specifications
 - To gain immediate feedback about the specification itself.
 - To do rapid prototyping
 - To test a specificand through symbolic execution of the specification
- Many examples
 - Statecharts
 - OBJ
 - Prolog, Paisley
 - Most recent ones

Characteristics 4. Tool-supported

- Model-Checking tools
 - Let users construct a finite-state model of the system
 - Then show a property holds in each state or state transition of the system
 - EMC, SMV, SPIN
- Proof-checking tools
 - Let users treat algebraic specifications as rewrite rules
 - Larch Prover, Affirm, Reve
 - Handling first-order logic
 - Boyer-Moore Theorem Prover, FDM, HDM, m-EVES
 - Handling higher-order logic
 - HOL, LCF, OBJ

Some Examples

Abstract Data Type: Z, VDM, Larch Concurrency: Temporal Logic, CSP, Transition Axioms

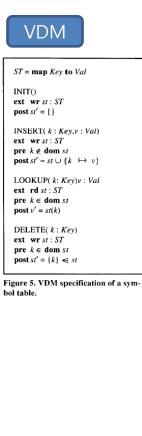
Some Examples

- 6 well-known formal methods (in 1990s)
 - Abstract data type : Z, VDM, Larch
 - Symbol table example
 - Concurrency : Temporal Logic, CSP, Transition Axioms
 - Unbounded buffer example
- When specifying the same problem with different methods, they look
 - Remarkably similar
 - Or totally different
 - Due to
 - Nature of the specificand
 - Simplicity of the specificand
 - Methods themselves

Abstract Data Type: Z, VDM, Larch

3 different specifications for a symbol table ullet

ST = KEY ↔ V	AL.
st' : ST	
st' = { }	
INSERT-	
st, st´: ST	
k : KEY	
v : VAL	
k∉ dom(st) ∕	
$st' = st \cup \{k \vdash$	→v}
LOOKUP	
st, st´: ST	
k : KEY	
$\mathbf{v}': \mathbf{VAL}$	
k ∈ dom(st) /	
v′ = st(k) ∧	
st´ = st	
DELETE	
st, st´: ST	
k : KEY	
k∈ dom(st) /	<u> </u>



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Larch

symbol_table is data type based on S from SymTab init = proc () returns (s: symbol_table) ensures $s' = emp \land new(s)$ insert = proc (s: symbol_table,k: key,v: val) requires ~ isin(s,k) modifies (s) ensures s' = add(s,k,v)lookup = proc (s: symbol_table,k : key) returns (v: val) requires isin(s, k) ensures v' = find(s,k)delete = proc (s: symbol_table, k : key) requires isin(s,k) modifies (s) ensures s' = rem(s,k)end symbol_table SymTab: trait introduces $emp: \rightarrow S$ add: S.K.V \rightarrow S rem: $S, K \rightarrow S$ find: $S, K \rightarrow V$ isin: $S, K \rightarrow Bool$ asserts S generated by (emp, add) S partitioned by (find, isin) for all (s: S,k,k1 : K,v : V) rem(add(s,k,v),k1) == if k = k1 then s else add(rem(s,k1),k,v)find(add(s,k,v),k1) == if k = k1 then v else find(s,k1)isin(emp,k) == false $isin(add(s.k.v).k1) == (k = k1) \lor isin(s.k1)$

implies converts (rem,find,isin) exempting (rem(emp),find(emp)) end SymTab

Figure 6. Larch specification of a symbol table.

Abstract Data Type: Z, VDM, Larch

	Z (1988)	VDM (1986)	Larch (1985)
Base	Model-oriented (Also property-oriented)	Model-oriented	Property-oriented
Readability	Good	Normal	Bad
Specifiability	Bad	Normal	Good
Size	Normal	Compact	Long
Tool-Support	Proof Checker B	N/A	Syntax Analyzer Larch Prover

Concurrency: Temporal Logic, CSP, Transition Axioms

• 3 different specifications for an **unbounded buffer**

Temporal Logic

$\langle right m \rangle \Rightarrow \Leftrightarrow \langle left m \rangle$	(1)
$(\langle right!m\rangle \land \ominus \Diamond \langle right!m\rangle) \Rightarrow \Diamond (\langle left!m\rangle \land \ominus \Diamond \langle left!m\rangle)$	(2)
$(\langle \text{left} m \rangle \land \bigoplus \diamondsuit \langle \text{left} m' \rangle) \Rightarrow (m \neq m')$	(3)
$(\langle left m\rangle) \Rightarrow \Diamond (\langle right m\rangle)$	(4)

Figure 7. Temporal logic specification of an unbounded buffer.

CSP

 $BUFFER = P_{<>}$ where $P_{<>} = \text{left}?m \rightarrow P_{<m>}$ and $P_{<m>}s_{s} = (\text{left}?n \rightarrow P_{<m>}s_{s})$ i right! $m \rightarrow Ps$)
BUFFER sat (right $\leq \text{left}$) \land (if right = left then left \notin ref else right \in ref)

Figure 8. CSP program and specification of an unbounded buffer.

Transition Axioms

module BUFFER with subroutines PUT, GET

state functions:

buffer : sequence of message parg : message or NULL gval : message or NULL

initial conditions:

|buffer| = 0

safety properties

```
    (a) at(PUT) ⇒ parg = PUT.PAR
    (b) after(PUT) ⇒ parg = NULL
    (a) at(GET) ⇒ gval = NULL
    (b) after(GET) ⇒ GET.PAR = gval
    allowed changes to buffer
        parg when in(PUT)
        gval when in(GET)
        (a) α[BUFFER]:in(PUT) ∧ parg ≠ NULL →
        parg' = NULL ∧ buffer' = buffer * parg
        (b) α[BUFFER]:in(GET) ∧ gval = NULL ∧ lbuffer|> 0 →
        gval' ≠ NULL ∧ buffer = gval' * buffer'
```

liveness properties

```
4. in(PUT) \land |buffer| < min \land after(PUT)
5. in(GET) \land |buffer| > 0 \land after(GET)
```

Figure 9. Transition axiom specification of an unbounded buffer.

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Concurrency: Temporal Logic, CSP, Transition Axioms

	Temporal Logic (1980)	CSP (1985)	Transition Axioms (1983)
Base	Property-oriented	Model-oriented (for specifying) Property-oriented (for proving)	Model-oriented (for specifying) Property-oriented (for proving)
Readability	Normal	Normal	Good
Specifiability	Bad	Bad	Good
Size	Compact	Compact	Long
Tool-Support	Many related tools	Proof Checker B	N/A

Bounds of Formal Methods

Between the Ideal and Real Worlds Assumptions about the Environment

Between the Ideal and Real Worlds

- Formal methods are
 - Based on mathematics
 - But not entirely mathematical
- Two important boundaries between the mathematical and the real world

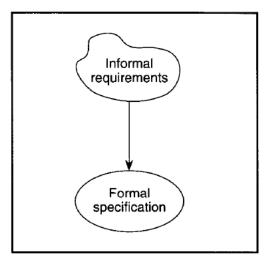


Figure 10. Mapping informal requirements for a formal specification.

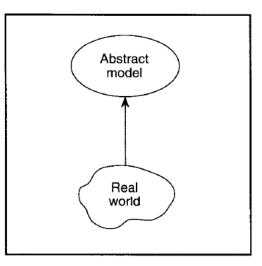


Figure 11. Mapping the real world to an abstract model.

Assumptions about the Environment

- There is a boundary between a real system and its environment
 - Environment is out of the scope of formal specifications (Open System)
 - Except, Gist specification language
 - Environment \Rightarrow System
 - *Environment* is a set of assumptions
 - System is a set of constraints on its behaviors placed by specifiers
 - Implicit assumptions in programming language areas
 - Specifiers should make explicit as many assumptions as possible.

• Hazard Analysis

- Identify a system's safety-critical components
 - FTA, FMEA, HAZOP
- A complementary technique to formal methods

Concluding Remarks

Formal Methods Challenges

Formal Methods

- In a strict mathematical sense,
 - Formal methods differ greatly from one another
- In a practical sense,
 - Formal methods do not differ radically from one another
- Formal methods can be used
 - 1. Identify
 - Deficiencies in informal requirements
 - Discrepancies between a specification and an implementation
 - Errors in existing programs and systems
 - 2. Specify
 - Medium-sized and nontrivial problems
 - Functional behavior
 - 3. Provide
 - Deeper understanding of the behavior of systems

Challenges

- 1. Specifying nonfunctional behavior
 - Reliability, safety, real-time, performance, human factors
- 2. Combining different methods
 - Domain specific + General
 - Formal + Informal
- 3. Building more usable and robust tools
 - Can manage large specifications
 - Can perform more complicated semantic analysis
- 4. Building specification libraries
 - Reuse in general or domain-specific purpose
- 5. Formal methods based software development
- 6. Scale up existing techniques
- 7. Educating and training